

S/N 09/523,132

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: OKAZAKI et al.

Examiner:

Serial No.: 09/523,132

Group Art Unit:

Filed: March 10, 2000

Docket No.:

Title: HIGH-FREQUENCY CIRCUIT ELEMENT

RECEIVED

DEC 10 2000

Unknown

2816

10873.506US\$

12/10/01

CERTIFICATE UNDER 37 CFR 1.10

'Express Mail' mailing label number: EV037644525US

Date of Deposit: October 30, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service 'Express Mail Post Office To Addressee' service under 37 CFR 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.

By:

Chris Stordahl

AMENDMENT AND RESPONSE UNDER 37 C.F.R. §1.111

Assistant Commissioner for Patents
Washington, D. C. 20231

Dear Sir:

In response to the Office Action mailed July 31, 2001 please amend the above-mentioned patent application as follows:

IN THE SPECIFICATION

Please amend the paragraphs starting at page 1 on line 19 and ending at page 2 on line 7 to read as follows:

As shown in Fig. 8, in the conventional high-frequency circuit element, $\lambda/2$ resonators 86a, 86b, 86c, 86d of a strip conductor pattern and input/output lines 87a, 87b are formed on the surface of the substrate 85 made of dielectric monocrystal, or the like. The high-frequency circuit having a microstrip structure is fabricated from $\lambda/2$ resonators 86a, 86b, 86c, 86d, input/output lines 87a, 87b and a ground plane 88. This high-frequency circuit includes four coupled $\lambda/2$ resonators and functions as a four-stage band pass filter.